COPY OF PAPERS ORIGINALLY FILED

DOCKET NO.: END919980110US3

Art Unit: 2813

Examiner: Berezny, Nema O.

3/13/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jimarez et al.
Serial No.: 09/782,471

Filed: 2/12/01

For: FLIP CHIP C4 EXTENSION STRUCTURE AND PROCESS

Commissioner for Patents Washington, D.C. 20231

Response to Restriction Requirement

Sir:

In response to the Restriction Requirement dated December 3, 2001, Applicant's hereby provisionally elect Group I, claims 1-18, drawn to a semiconductor device, classified in class 257, subclass 734. This election is made with traverse, and Applicant's hereby reserve the right to file a divisional application in connection with unelected claims 19-39 drawn to a method of making a semiconductor device.

With regard to the Restriction Requirement, Applicant's respectfully submit that the subject matter of all claims 1-39 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicant's respectfully submit that the search and the examination of the entire application could be made without serious burden. See MPEP § 803, in which it is stated that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits" (emphasis added). Applicant's respectfully submit that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

Should the Examiner require or request anything further from Applicant's prior to examination, the Examiner is requested to contact Applicants' undersigned representative at the telephone number below. Otherwise, Applicant's request early and favorable examination on the merits.

Jack P. Friedman

Reg. No. 44,688

Dated: December 20, 2001

Schmeiser, Olsen & Watts 3 Lear Jet Lane - Suite 201 Latham, New York 12110 (518) 220-1850 FEB 2 6 2002

TRANSMITTAL LETTER (General - Patent Pending)

Docket No. END919980110US3

intervals ication Of: Jimarez et al.

Serial No. 09/782.471

Filing Date 2/12/01

Examiner Berezny, Nema O.

28 Grand Unit

Title: FLIP CHIP C4 EXTENSION STRUCTURE AND PROCESS

AIL ROOM

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:

Transmitted herewith is:

Response to Restriction Requirement

in the above identified application.

- No additional fee is required.
 - A check in the amount of

is attached.

- The Commissioner is hereby authorized to charge and credit Deposit Account No. 09-0457(IBM as described below. A duplicate copy of this sheet is enclosed.
 - Charge the amount of
 - Credit any overpayment.
 - Charge any additional fee required.

Jack P. Friedran

Dated: 12/20/01

Jack P. Friedman Reg. No. 44,688 Schmeiser, Olsen & Watts 3 Lear Jet Lane, Suite 201 Latham, NY 12110 (518) 220-1850

I certify that this document and fee is being deposited on 12/20/01 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Kim Dwileski

Typed or Printed Name of Person Mailing Correspondence

CC: